	Application No.	Applicant(s)
Notice of Allowability	10/785,677	FOSTER ET AL.
	Examiner	Art Unit
	Mark E. Wallerson	2626
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to the RCE filed on 5/23/05.		
2. The allowed claim(s) is/are <u>1-24.</u>		
3. The drawings filed on 24 February 2004 are accepted by the Examiner.		
4.		
 Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date <u>5/23/05</u> 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☐ Examiner's Amendn	

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Part III REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance. The prior art of record does not teach, disclose, or suggest the claimed limitations of (in combination with all other limitations in the claim), an electrostatic method for printing an image on a receiver by operating a primary charger to establish a uniform primary voltage level on an image recording member; developing a control patch on the image recording member, measuring density of the control patch to provide a density measurement signal, converting the image into a digital bitmap comprised of an array of pixels wherein each pixel is assigned a digital value representing marking information; rendering the digital bitmap by defining each pixel as either a background pixel, interior pixel, or an edge pixel and reassigning the digital value of one or more of edge pixels or interior pixels independently and as a function of the density measurement signal and modulating electrostatic charge on the image recording member as a function of the digital bitmap after rendering as claimed in claim 1. Similar features are claimed in claims 7, 13, and 19. Therefore, claims 7, 13, and 19 are allowable over the prior art of record for the same reasons.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mark E. Wallerson whose telephone number is (571) 272-7470. The examiner can normally be reached on Monday-Friday - 6:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kimberly Williams can be reached on (571) 272-7471. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mark E. Wallerson Primary Examiner

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MARK WALLERSON PRIMARY EXAMINER